

<b>Notice of References Cited</b>	Application/Control No. 10/627,977	Applicant(s)/Patent Under Reexamination WOZNIAK, ANDRZEJ	
	Examiner DAVID SILVER	Art Unit 2128	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	Yalamanchili, Sudhakar, "VHDL: From Simulation to Synthesis", School of Electrical and Computer Engineering, Georgia Institute of Technology, 4 February 2000
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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